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CERTIFICATE OF TRANSMISSION

January 9, 2004

Atty Docket No. :	JCLA8533
Appl. No. :	10/055,568
Filing Date :	January, 22, 2002
Pages :	Cover + 1

BY FACSIMILE ONLY

Fax No. :	703-872-9306
Attention :	EXAMINER : MITCHELL, JAMES M Commissioner for Patents
Group Unit :	2827
From :	Jiawei Huang, Reg. No. 43,330
MESSAGE :	Enclosed herewith is a Response to Restriction Requirement in 1 page.

Sir:

I hereby certify that this correspondence is being facsimile transmitted to the Patent and Trademark Office on January 9, 2004 at the above indicated fax number.

Sign by:


Michelle Chang

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PATENT

Docket No.: JCLA8533

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of :

Applicant : MOU-SHIUNG LIN et al.

Application No. : 10/055,568

Filed : January 22,2002

For : INTEGRATED CHIP PACKAGE STRUCTURE USING SILICON
SUBSTRATE AND METHOD OF MANUFACTURING THE SAME

Art Unit : 2827

Examiner : MITCHELL, JAMES M

RESPONSE TO RESTRICTION REQUIREMENT**MAIL STOP Non-Fee Amendment**

Commissioner for Patents

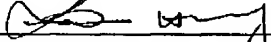
P.O. Box 1450

Alexandria, VA 22313-1450

Dear Sir:

In response to the Restriction Requirement mailed on December 10, 2003 regarding the above-identified application, Applicant elects Species of Figure 6, claims 1-17, 21, 23-29, and 139-140, for further examination on merits. Please cancel claims 30-40, 43-48, 52, 52-60 and 141-142 without prejudice and disclaimer.

No fee is believed to be due in connection with the filing of this paper. However, the Commissioner is authorized to charge any additional fees that may be required to Account No. 50-0710 (Order No. JCLA8533).

Respectfully submitted,
J.C. PATENTSDated: 1/9/2004By: Jiawei Huang
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